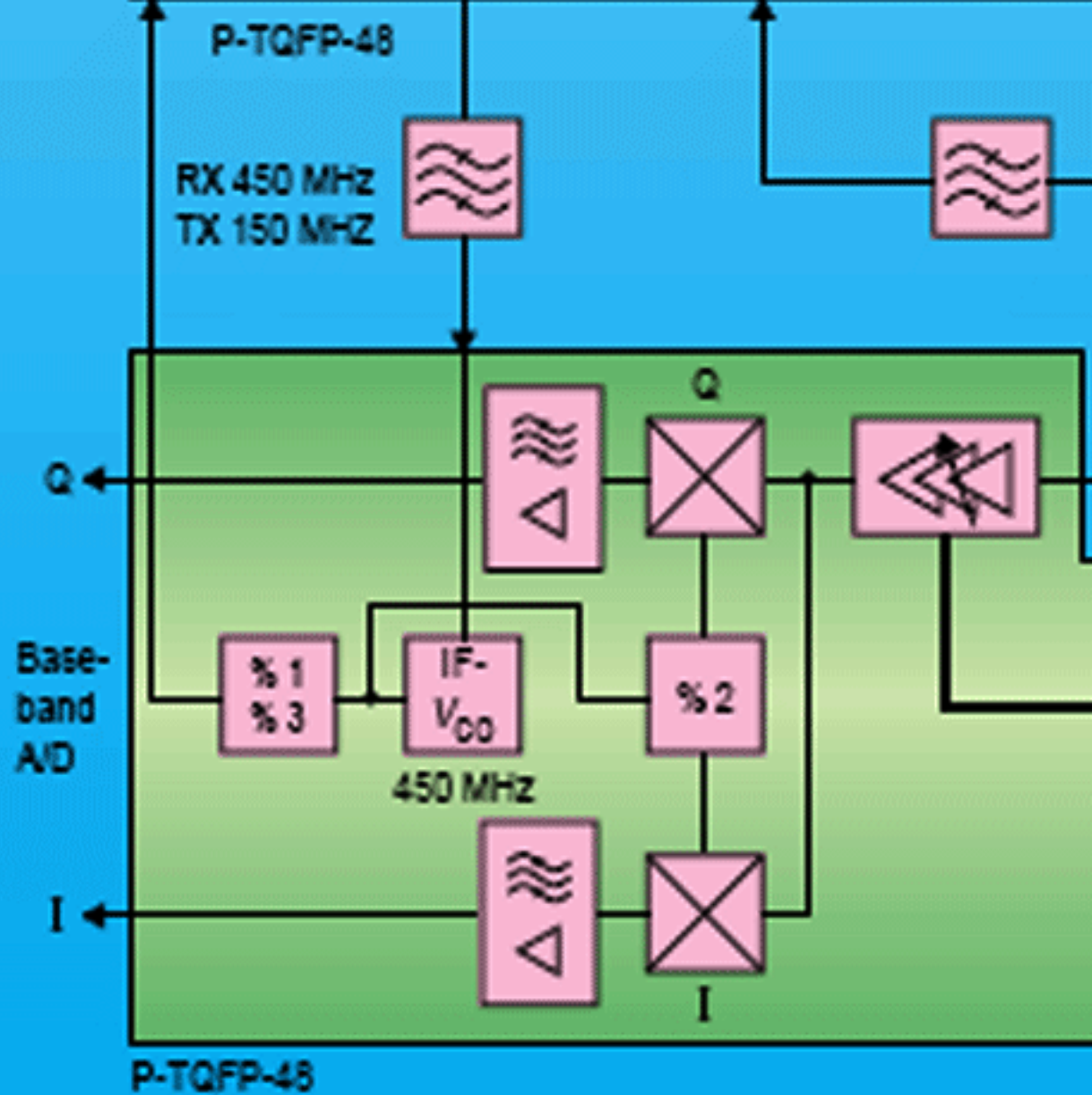
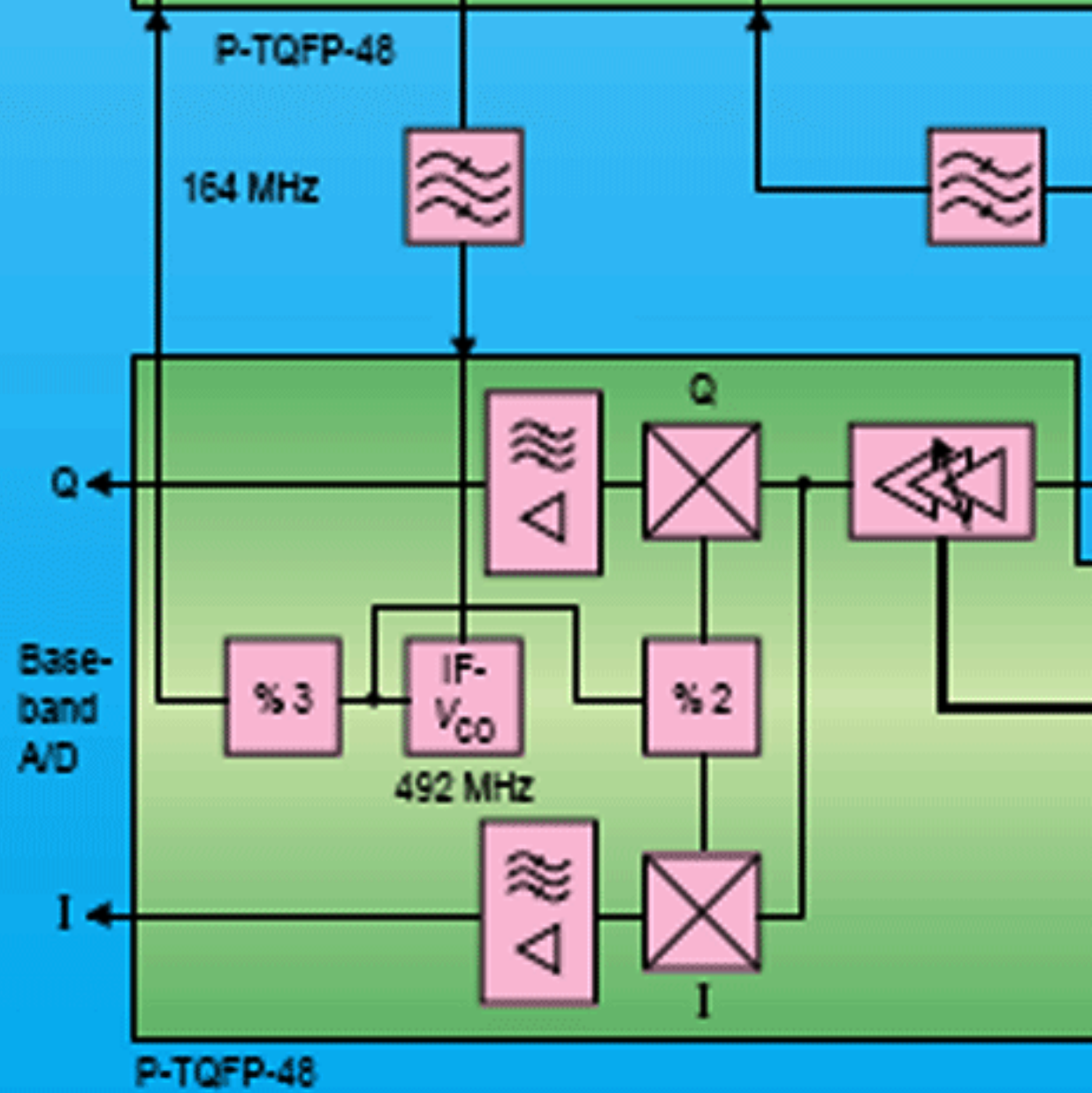


The second local oscillator signal LO2 is generated either by an on-chip oscillator or by an external VCO. The internal oscillator signal is fed to a divider (PMB 2405-by 1; 2407-by 1 or 3) and then to a buffered output and also to a divider, which generates orthogonal signals at half the VCO frequency. The filtered IF signal re-enters the chip at the IF input, where it is amplified and converted to the final output frequency with each of the orthogonal signals. The resulting in-phase and quadrature signals pass through differential output drivers. Two differential operational amplifiers can be used as baseband filters. At both outputs the differential offset is sensed via the sample and hold circuitry. A feedback loop corrects the remaining offset error below the tolerable value of the GAIM PMB 2905 or any other baseband A/D converter.



PCS Application PMB 2247 / PMB 2407 / PMB 23



PCN Application PMB 2245 / PMB 2407 / PMB 2